

IN THE CLAIMS:

1. (Cancelled)
2. (Previously Presented) A method in a data processing system for providing valid translation entries in a translation control entry table for all supported direct memory addresses, comprising:
 - reserving a page in system memory to form a reserved page;
 - writing the reserved page;
 - selecting a region in the system memory for the translation control entry table;
 - initializing all entries in the translation control entry table, wherein all entries are initialized to be valid and contain an address of the reserved page; and
 - updating an entry in the translation control entry table, wherein a physical memory page replaces the reserved page when the entry is used by an operating system's device driver.
3. (Original) The method of claim 2, further comprising:
 - restoring the entry in the translation control entry table with the reserved page when the entry is no longer used by an operating system.
4. (Previously Presented) The method of claim 2, further comprising:
 - determining whether a direct memory address translation corresponding to the entry has been cached in a translation lookaside buffer; and
 - responsive to a determination that the direct memory address translation exists, clearing the direct memory address translation from the translation lookaside buffer.
5. (Previously Presented) The method of claim 2, wherein the page in the system memory is inaccessible to an operating system running on the data processing system.
6. (Previously Presented) The method of claim 2, wherein writing the reserved page includes setting all bytes within the reserved page to 0xFF.

7. (Previously Presented) The method of claim 2, wherein initializing all entries to be valid includes setting all valid bits to “1”.

8. (Cancelled)

9. (Previously Presented) The method of claim 2, wherein the translation control entry table comprises a 2MB Translation Control Entry table having 512K 4-byte entries.

10-27. (Cancelled)

28. (Previously Presented) The method of claim 2, wherein the reserved page is utilized for DMA address translation.

29-30. (Cancelled).